

Virtual Platform Design and Verification Engineer

Job Code: MB_SystemC_Noida

Job Description

Looking for Engineer doing or can do design and verification of Virtual Platform for embeded software development and SystemC/ C++ model of processor and its peripheral such as USB, Ethernet, SLIMbus etc.

Desired Profile

- Proficiency in C, C++, data structure, algorithm, graph theory
- Minimum one to maximum two years of industry experience in C++ coding
- Should have fundamental knowledge of processor architecture, peripheral interface, different bus architecture
- Exposure to systemC will be an added advantage with preference of ARM cores knowledge
- Good knowledge of make, C++ debugger and unix
- Work experience with Virtual Platform development, integration, release is a plus
- Good communication skill set, good team player

Qualifications

Education:

- Bachelor Degree / Masters in EC or CS

Experience / Knowledge:

- 1 to 2 years of industry experience with C++ coding and OOPs concepts

Required Experience: This job is suitable for experienced applicants

Location

Noida

To Apply

Send your resume to hr@masamb.com with the position name in the subject heading

Customers

Synopsys, Freescale, Mentor Graphics, STMicroelectronics are our major customers for project on which the selected engineers will work