



Masamb is a technology focussed Semiconductor Design services company. Masamb has development centers in Noida and Bangalore. The company has strong expertise in the areas of Verification (SoC Functional Verification, OVM/VMM, SystemC/Virtual platforms), IPs (Standard Cell, IOs, Memories IP library), Low Power Design (working on complex SoC, CPF/UPF based flow from RTL to GDSII)

FE Synthesis and Timing Analysis Tech Lead (Exp 5+yr)

Job Code: MB_Mem_STA_Noida

Job Description

The job involves FE Synthesis Timing Analysis. Mentoring the team and doing hands on to manage effective execution of deliverables

Desired Profile

- 5 yrs+ experience in Front end Synthesis, STA and timing closure
- Ability to handle a team of engineers.
- Having good leadership quality.
- Expertise in synthesizing multiple RTL blocks.
- Knowledge of Scan Chain Synthesis, SDF Back Annotation.
- Scan Chain Pre/Post Synthesis Analysis.
- Ability to understand and analyze RTL written in Verilog/VHDL.
- Good experience with Design Compiler, RC and Conformal.
- Exposure on writing Perl/TCL scripts.
- Knowledge of Memory modeling would be beneficial.
- Knowledge of DFT will be one plus
- Tools used: Synopsys DC Design Compiler, Design Time, Prime Time, Time Mill, Path Mill
- Good communication skill set, good team player

Qualifications	<p>Education:</p> <ul style="list-style-type: none"> •UG - B.Tech/B.E. - Computers, Electrical, Electronics/Telecommunication PG - M.Tech - Electrical, Electronics/Telecommunication <p>Experience / Knowledge:</p> <ul style="list-style-type: none"> • 5+ years of industry experience in Synthesis and Timing Analysis, having good team leadership skills <p>Required Experience: This job is suitable for experienced applicants</p>
Location	Noida
To Apply	Send your resume to hr@masamb.com with the position name in the subject heading
About Masamb	<p>Masamb is a technology focussed Semiconductor Design services company. Masamb has development centers in Noida and Bangalore. The company is building strong expertise in the areas of Verification(SoC Functional Verification, OVM/VMM, SystemC/Virtual platforms), IPs(Standard Cell, IOs, Memories IP library development, Verification IPs); Low Power Design(working on complex SoC, CPF/UPF based flow from RTL to GDSII)</p> <ul style="list-style-type: none"> • Masamb is led by industry veterans from the semiconductor industry having held senior management positions in companies like Freescale, Cadence, Synopsys, SCL Mohali • Masamb startup Culture offers a great deal of flexibility, fast and customized career growth plan and learning atmosphere, and helps us to build strong skills in niche domain • Masamb has been growing steadily and consistently over the last three years. • Masamb engineers have been working on challenging projects for the leading Semicon and EDA companies. Masamb is steadily moving up the Value Chain, significantly working on complex and challenging projects where the complete ownership of end to end execution and deliverables lies with Masamb. Synopsys, Freescale, Mentor Graphics, STMicroelectronics are our major customers for project on which the selected engineers will work